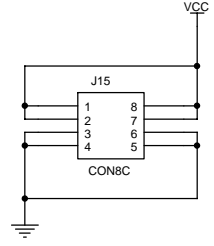
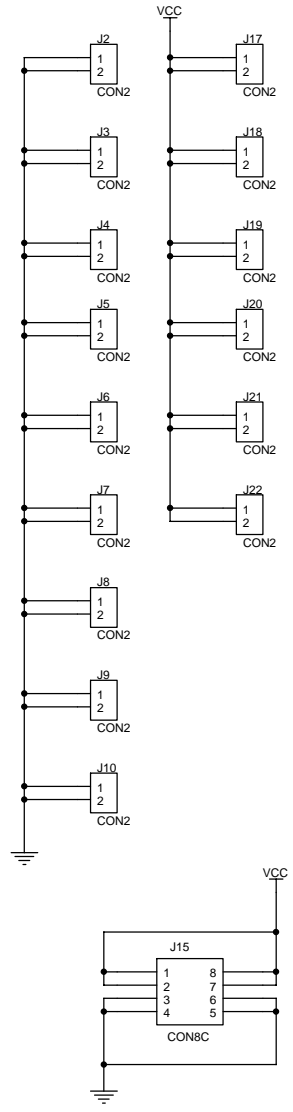
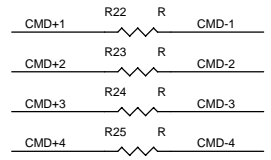
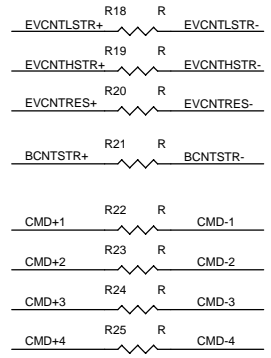
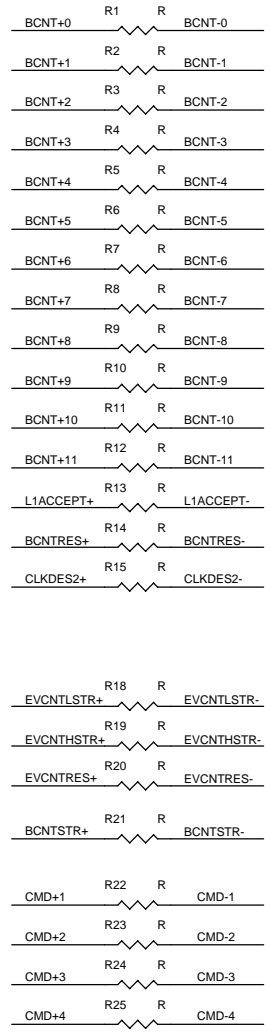
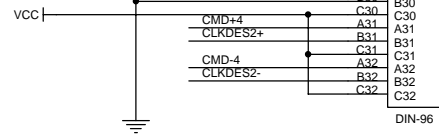


Signal	Pin
BCNT+0	A1
BCNT+1	B1
BCNT+2	C1
BCNT-0	A2
BCNT-1	B2
BCNT-2	C2
BCNT+3	A3
BCNT+4	B3
BCNT+5	C3
BCNT-3	A4
BCNT-4	B4
BCNT-5	C4
BCNT+6	A5
BCNT+7	B5
BCNT+8	C5
BCNT-6	A6
BCNT-7	B6
BCNT-8	C6
BCNT+9	A7
BCNT+10	B7
BCNT+11	C7
BCNT-9	A8
BCNT-10	B8
BCNT-11	C8
EVCNTLSTR+	A9
EVCNTHSTR+	B9
EVCNTRES+	C9
EVCNTLSTR-	A10
EVCNTHSTR-	B10
EVCNTRES-	C10
L1ACCEPT+	A11
BCNTRES+	B11
BCNTSTR+	C11
L1ACCEPT-	A12
BCNTRES-	B12
BCNTSTR-	C12
CMD+1	A13
CMD+2	B13
CMD+3	C13
CMD-1	A14
CMD-2	B14
CMD-3	C14
A15	A15
B15	B15
C15	C15
A16	A16
B16	B16
C16	C16
A17	A17
B17	B17
C17	C17
TCLK+5	A18
RCLK+5	B18
TCLK-5	C18
RCLK-5	A19
TCLK+4	B19
RCLK-4	C19
TCLK-4	A20
RCLK+4	B20
TCLK-4	C20
RCLK-4	A21
TCLK+3	B21
RCLK-3	C21
TCLK+3	A22
RCLK+3	B22
TCLK-3	C22
RCLK-3	A23
TCLK+2	B23
RCLK-2	C23
TCLK+2	A24
RCLK-2	B24
TCLK-2	C24
RCLK+2	A25
TCLK+2	B25
RCLK-2	C25
TCLK+1	A26
RCLK+1	B26
TCLK-1	C26
RCLK-1	A27
TCLK+0	B27
RCLK-0	C27
TCLK+0	A28
RCLK+0	B28
TCLK-0	C28
RCLK-0	A29
C29	B29
C29	A30
B30	C30
B30	A31
C30	B31
CMD+4	A31
CLKDES2+	B31
CMD-4	A32
CLKDES2-	B32
C32	A32
C32	B32
C32	C32



Title		
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