TIM Backplane. V2.2b

27-10-2006

(blue=common lines, 100 ohms terminated on backplane; red= point-to-point, to be terminated on ROS or TRB; blank=GND)

TIM Connector

pin	signal	pin	signal	pin	signal
A1	BCNT_P_0	B1	BCNT_P_1	C1	BCNT_P_2
A2	BCNT_N_0	B2	BCNT_N_1	C2	BCNT_N_2
A3	BCNT_P_3	B3	BCNT_P_4	C3	BCNT_P_5
A4	BCNT_N_3	B4	BCNT_N_4	C4	BCNT_N_5
A5	BCNT_P_6	B5	BCNT_P_7	C5	BCNT_P_8
A6	BCNT_N_6	B6	BCNT_N_7	C6	BCNT_N_8
A7	BCNT_P_9	B7	BCNT_P_10	C7	BCNT_P_11
A8	BCNT_N_9	B8	BCNT_N_10	C8	BCNT_N_11
A9	EVCNTL_STR_P	B9	EVCNTH_STR_P	C9	EVCNT_RES_P
A10	EVCNTL_STR_N	B10	EVCNTH_STR_N	C10	EVCNT_RES_N
A11	L1_ACCEPT_P	B11	BCNT_RES_P	C11	BCNT_STR_P
A12	L1_ACCEPT_N	B12	BCNT_RES_N	C12	BCNT_STR_N
A13	CMD1_P	B13	CMD2_P	C13	CMD3_P
A14	CMD1_N	B14	CMS2_N	C14	CMS3_N
A15		B15		C15	
A16		B16		C16	
A17		B17		C17	
A18	TCLK_P_5	B18		C18	RCLK_P_5
A19	TCLK_N_5	B19		C19	RCLK_N_5
A20	TCLK_P_4	B20		C20	RCLK_P_4
A21	TCLK_N_4	B21		C21	RCLK_N_4
A22	TCLK_P_3	B22		C22	RCLK_P_3
A23	TCLK_N_3	B23		C23	RCLK_N_3
A24	TCLK_P_2	B24		C24	RCLK_P_2
A25	TCLK_N_2	B25		C25	RCLK_N_2
A26	TCLK_P_1	B26		C26	RCLK_P_1
A27	TCLK_N_1	B27		C27	RCLK_N_1
A28	TCLK_P_0	B28		C28	RCLK_P_0
A29	TCLK_N_0	B29		C29	RCLK_N_0
A30		B30		C30	VCC
A31	CMD4_P	B31	CLKDES2_P	C31	VCC
A32	CMD4_N	B32	CLKDES2_N	C32	VCC

TRG Connector

pin	signal	pin	signal	pin	signal
A1	BCNT_P_0	B1	BCNT_P_1	C1	BCNT_P_2
A2	BCNT_N_0	B2	BCNT_N_1	C2	BCNT_N_2
A3	BCNT_P_3	B3	BCNT_P_4	C3	BCNT_P_5
A4	BCNT_N_3	B4	BCNT_N_4	C4	BCNT_N_5
A5	BCNT_P_6	B5	BCNT_P_7	C5	BCNT_P_8
A6	BCNT_N_6	B6	BCNT_N_7	C6	BCNT_N_8
A7	BCNT_P_9	B7	BCNT_P_10	C7	BCNT_P_11
A8	BCNT_N_9	B8	BCNT_N_10	C8	BCNT_N_11
A9	EVCNTL_STR_P	B9	EVCNTH_STR_P	C9	EVCNT_RES_P
A10	EVCNTL_STR_N	B10	EVCNTH_STR_N	C10	EVCNT_RES_N
A11	L1_ACCEPT_P	B11	BCNT_RES_P	C11	BCNT_STR_P
A12	L1_ACCEPT_N	B12	BCNT_RES_N	C12	BCNT_STR_N
A13	CMD1_P	B13	CMD2_P	C13	CMD3_P
A14	CMD1_N	B14	CMS2_N	C14	CMS3_N
A15	TRG_DATA_P_0	B15	TRG_DATA_P_1	C15	TRG_DATA_P_2
A16	TRG_DATA_N_0	B16	TRG_DATA_N_1	C16	TRG_DATA_N_2
A17	TRG_DATA_P_3	B17	TRG_DATA_P_4	C17	TRG_DATA_P_5
A18	TRG_DATA_N_3	B18	TRG_DATA_N_4	C18	TRG_DATA_N_5
A19	TRG_DATA_P_6	B19	TRG_DATA_P_7	C19	TRG_DATA_P_8
A20	TRG_DATA_N_6	B20	TRG_DATA_N_7	C20	TRG_DATA_N_8
A21	TRG_DATA_P_9	B21	TRG_DATA_P_10	C21	TRG_DATA_P_11
A22	TRG_DATA_N_9	B22	TRG_DATA_N_10	C22	TRG_DATA_N_11
A23	TRG_DATA_P_12	B23	TRG_DATA_P_13	C23	TRG_DATA_P_14
A24	TRG_DATA_N_12	B24	TRG_DATA_N_13	C24	TRG_DATA_N_14
A25	TRG_DATA_P_15	B25	TRG_STROBE_P	C25	ROS_READY_P
A26	TRG_DATA_N_15	B26	TRG_STROBE_N	C26	ROS_READY_N
A27		B27		C27	
A28	CLK_P	B28		C28	AUTO_L1A_P
A29	CLK_N	B29		C29	AUTO_L1A_N
A30		B30		C30	VCC
A31	CMD4_P	B31	CLKDES2_P	C31	VCC
A32	CMD4_N	B32	CLKDES2_N	C32	VCC

ROS Connector

pin	signal	pin	signal	pin	signal
A1	BCNT_P_0	B1	BCNT_P_1	C1	BCNT_P_2
A2	BCNT_N_0	B2	BCNT_N_1	C2	BCNT_N_2
A3	BCNT_P_3	B3	BCNT_P_4	C3	BCNT_P_5
A4	BCNT_N_3	B4	BCNT_N_4	C4	BCNT_N_5
A5	BCNT_P_6	B5	BCNT_P_7	C5	BCNT_P_8
A6	BCNT_N_6	B6	BCNT_N_7	C6	BCNT_N_8
A7	BCNT_P_9	B7	BCNT_P_10	C7	BCNT_P_11
A8	BCNT_N_9	B8	BCNT_N_10	C8	BCNT_N_11
A9	EVCNTL_STR_P	B9	EVCNTH_STR_P	C9	EVCNT_RES_P
A10	EVCNTL_STR_N	B10	EVCNTH_STR_N	C10	EVCNT_RES_N
A11	L1_ACCEPT_P	B11	BCNT_RES_P	C11	BCNT_STR_P
A12	L1_ACCEPT_N	B12	BCNT_RES_N	C12	BCNT_STR_N
A13	CMD1_P	B13	CMD2_P	C13	CMD3_P
A14	CMD1_N	B14	CMS2_N	C14	CMS3_N
A15	TRG_DATA_P_0	B15	TRG_DATA_P_1	C15	TRG_DATA_P_2
A16	TRG_DATA_N_0	B16	TRG_DATA_N_1	C16	TRG_DATA_N_2
A17	TRG_DATA_P_3	B17	TRG_DATA_P_4	C17	TRG_DATA_P_5
A18	TRG_DATA_N_3	B18	TRG_DATA_N_4	C18	TRG_DATA_N_5
A19	TRG_DATA_P_6	B19	TRG_DATA_P_7	C19	TRG_DATA_P_8
A20	TRG_DATA_N_6	B20	TRG_DATA_N_7	C20	TRG_DATA_N_8
A21	TRG_DATA_P_9	B21	TRG_DATA_P_10	C21	TRG_DATA_P_11
A22	TRG_DATA_N_9	B22	TRG_DATA_N_10	C22	TRG_DATA_N_11
A23	TRG_DATA_P_12	B23	TRG_DATA_P_13	C23	TRG_DATA_P_14
A24	TRG_DATA_N_12	B24	TRG_DATA_N_13	C24	TRG_DATA_N_14
A25	TRG_DATA_P_15	B25	TRG_STROBE_P	C25	ROS_READY_P
A26	TRG_DATA_N_15	B26	TRG_STROBE_N	C26	ROS_READY_N
A27		B27		C27	
A28	CLK_P	B28		C28	AUTO_L1A_P
A29	CLK_N	B29		C29	AUTO_L1A_N
A30		B30		C30	VCC
A31	CMD4_P	B31	CLKDES2_P	C31	VCC
A32	CMD4_N	B32	CLKDES2_N	C32	VCC